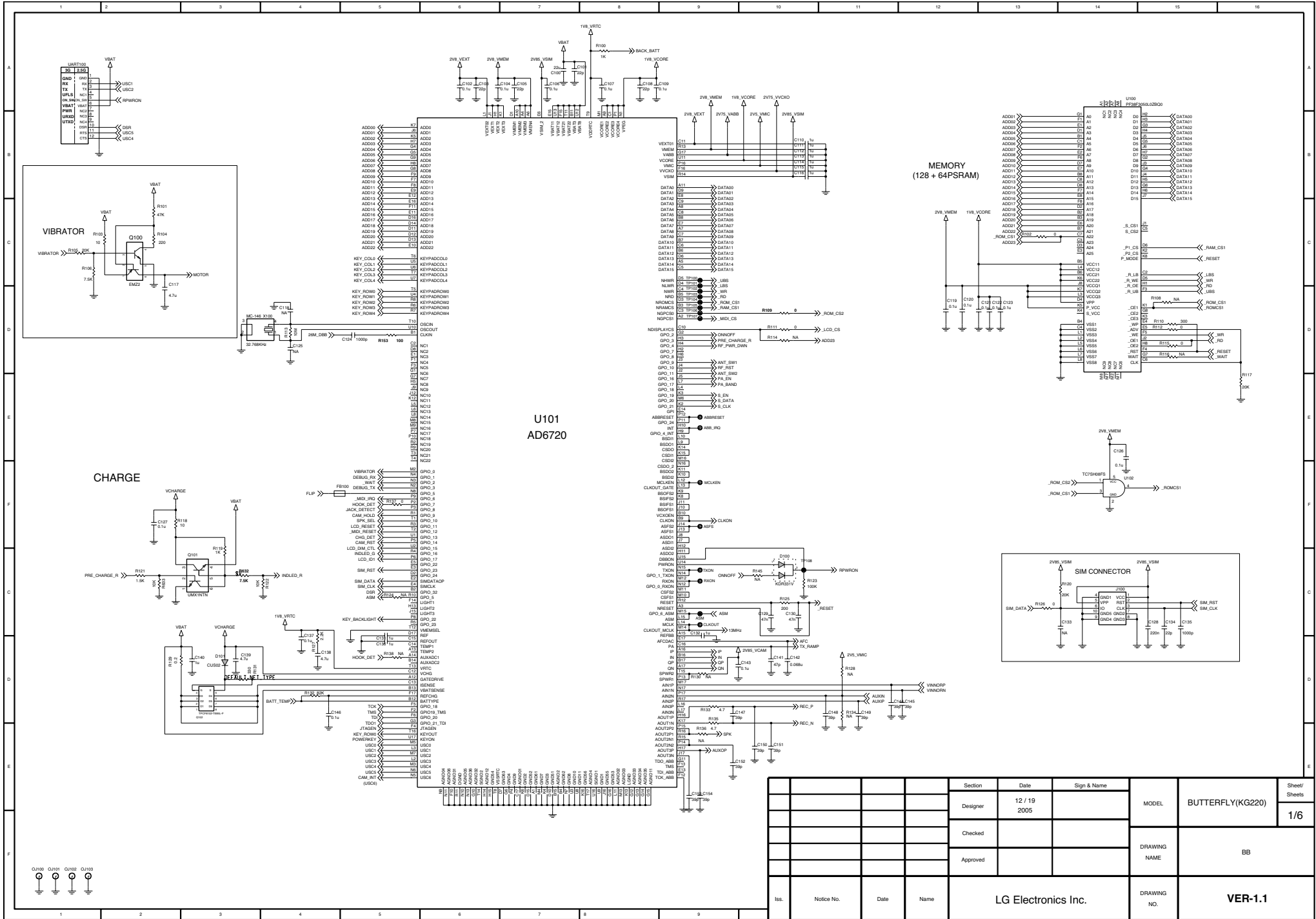
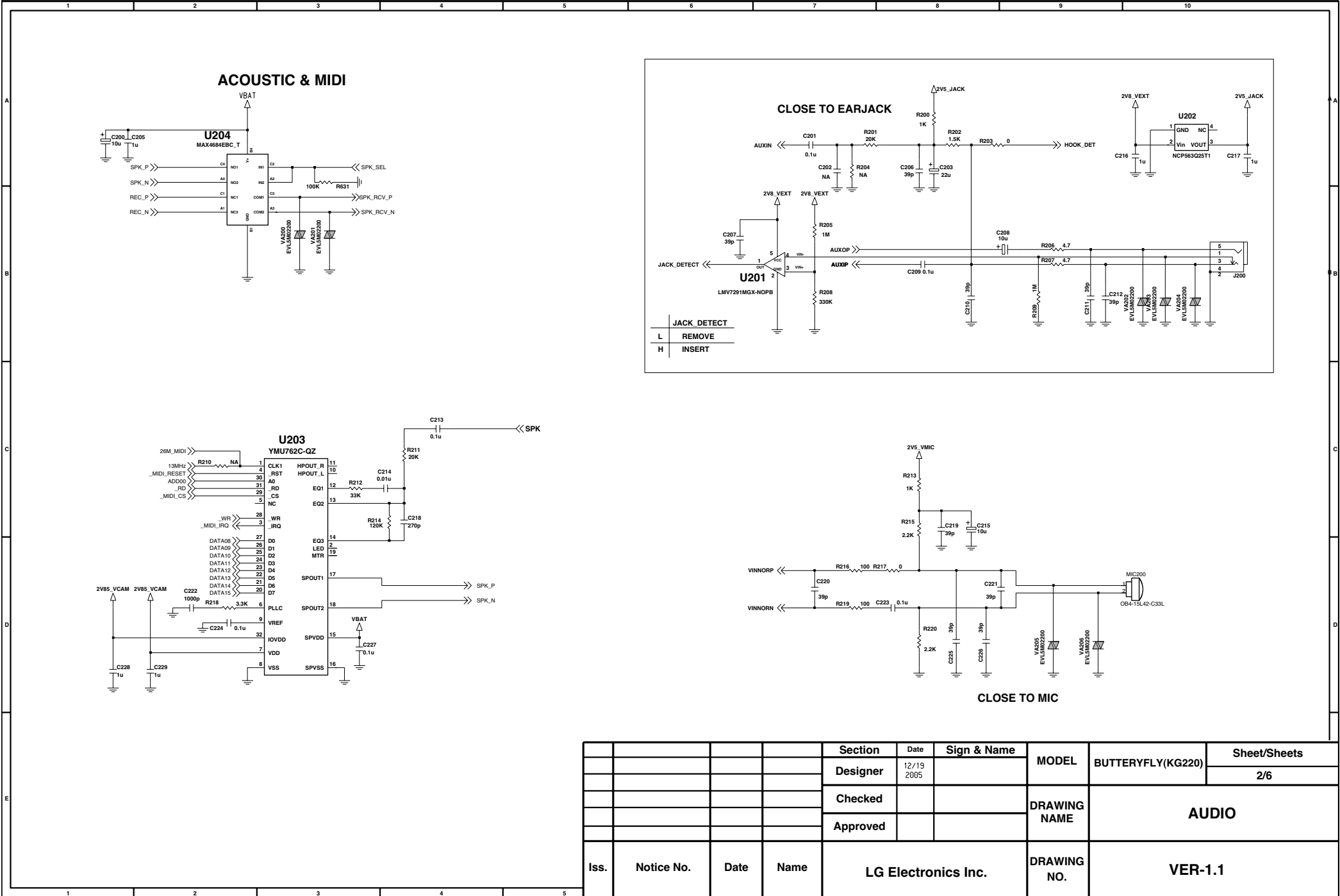


7. CIRCUIT DIAGRAM

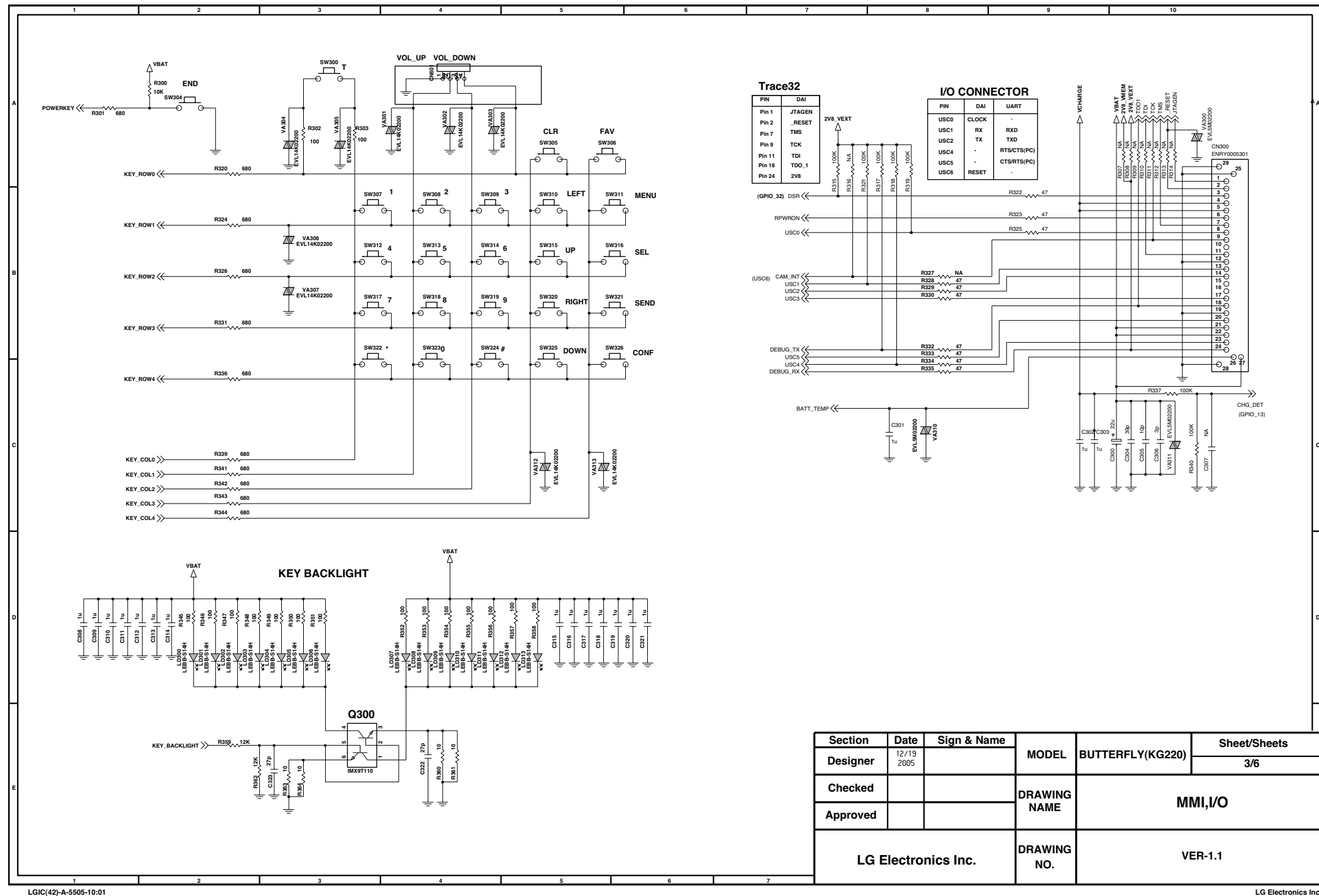


7. CIRCUIT DIAGRAM

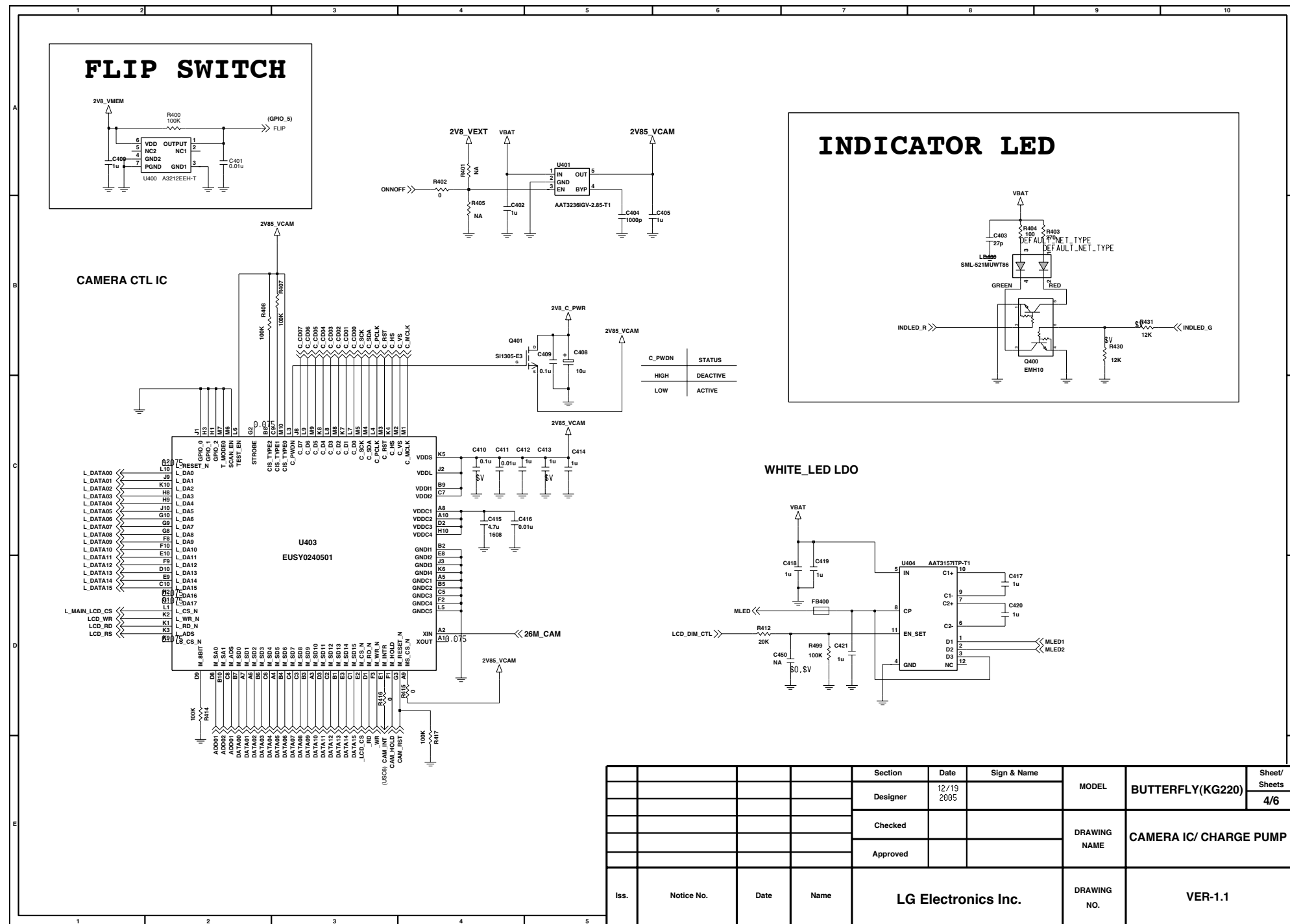


				Section	Date	Sign & Name	MODEL	BUTTERFLY(KG220)	Sheet/Sheets
				Designer	12/19 2005				2/6
				Checked			DRAWING NAME	AUDIO	
				Approved					
Iss.	Notice No.	Date	Name	LG Electronics Inc.			DRAWING NO.	VER-1.1	

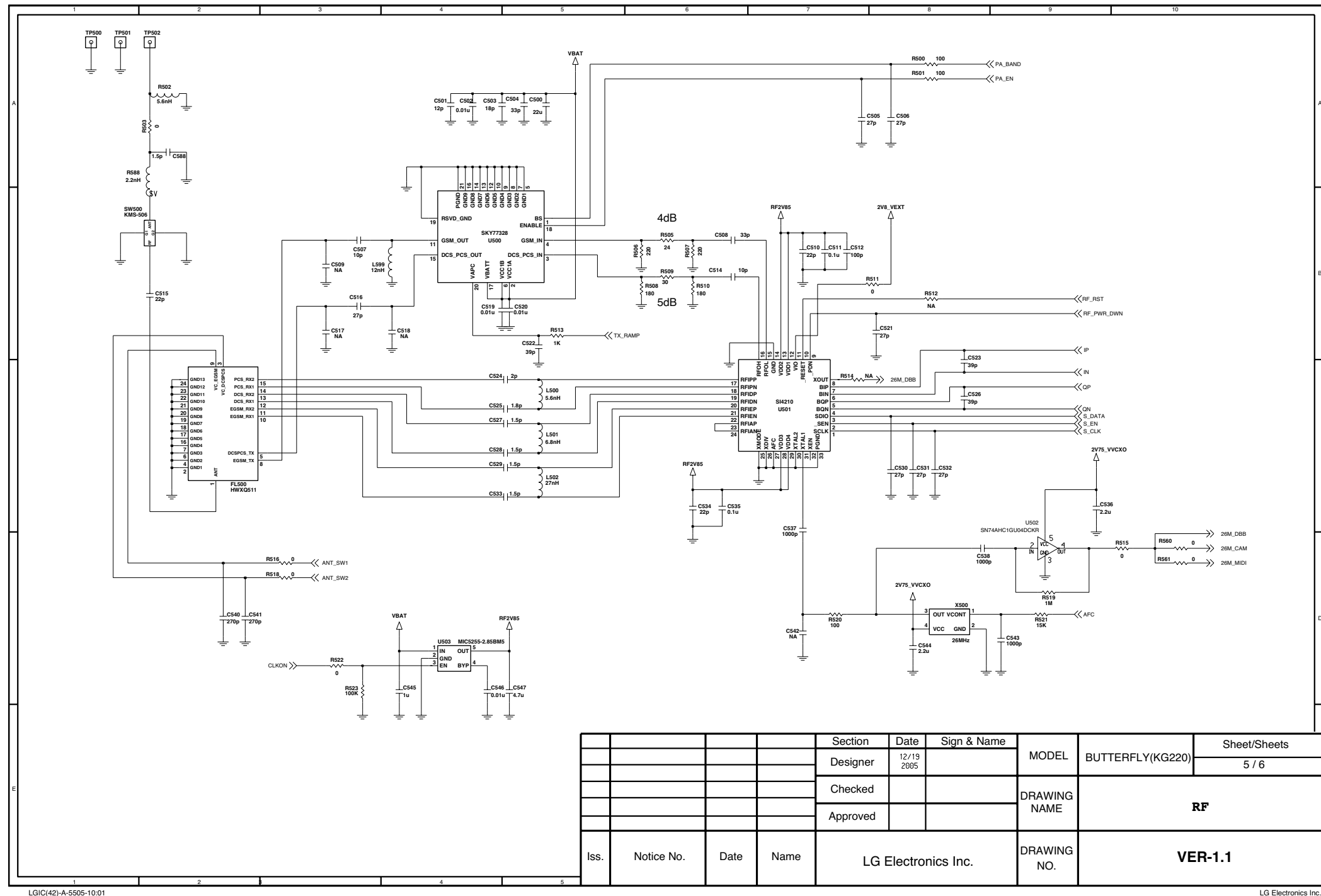
## 7. CIRCUIT DIAGRAM



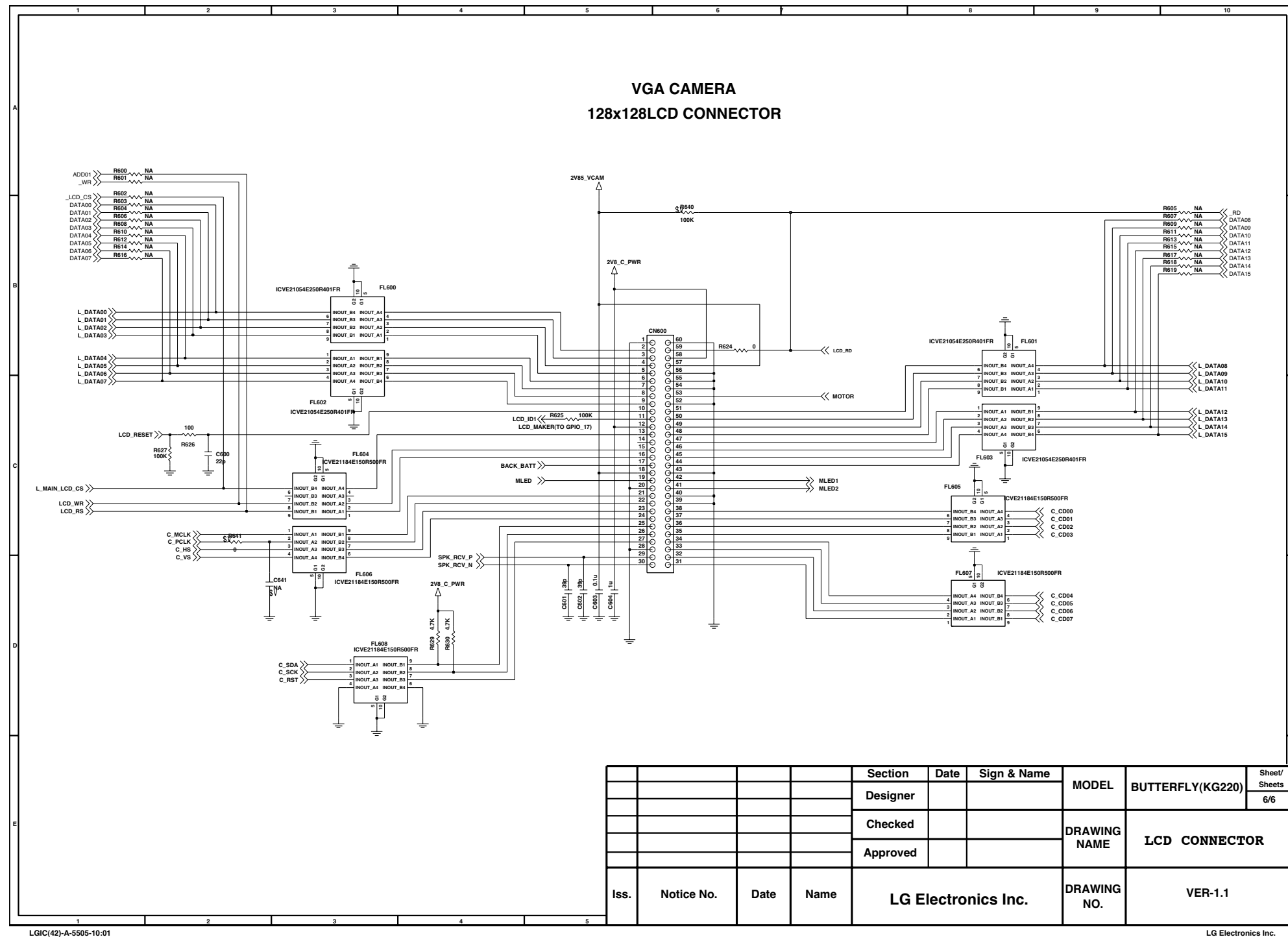
## 7. CIRCUIT DIAGRAM



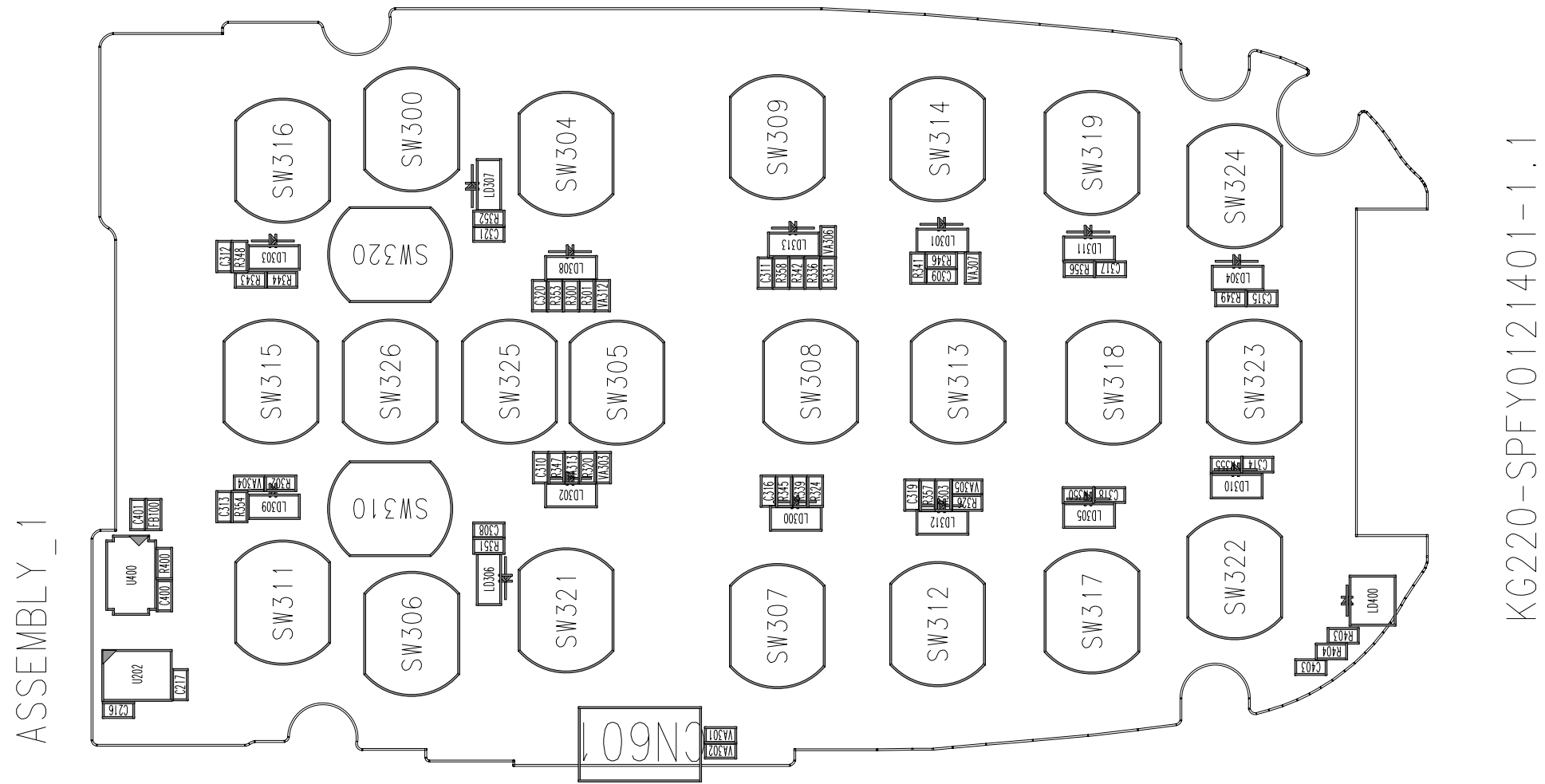
## 7. CIRCUIT DIAGRAM



## 7. CIRCUIT DIAGRAM



## 8. PCB LAYOUT



8. PCB LAYOUT

